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**1310 nm TX / 1550 nm TX / 3 Gb/s Medium Power 1-Fibre BIDI SMDigital  
Diagnostic Dual Transmitter**

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**FVD1-2T-SM10**

**Features**

- SMPTE 297-2006 Compatible
- 1-Fibre BIDI Dual Transmitter with Simplex LC
- Speed from 50 Mbps to 3Gbps with up to 30 km
- Distance up to 10 km for 3G-SDI
- Support Video Pathological Patterns for SD-SDI, HD-SDI and 3G-SDI
- Based on Industry Standard SFP
- SFF-8472 Digital Diagnostic Function
- Two Independent TTL Laser Disable input
- Single 3.3V power supply
- Operating case temperature : 0°C to 70°C
- ROHS Compliant
- Hot Pluggable
- Class 1 Laser International Safety Standard IEC-60825 Compliant



**Applications**

- SMPTE 297-2006 Compliant Electrical-to-Optical Interfaces
- High-density Video Routers

**Description**

The FVD1-2T-SM10 is a 1-fibre dual channel optical single mode transmitter module designed to transmit optical serial digital signals as defined in SMPTE 297-2006. It supports from 50 Mbps to 3 Gbps and is specifically designed for transmitted the SMPTE 259M, SMPTE 344M, SMPTE 292M and SMPTE 424M SDI pathological patterns. It is with the SFP 20-pin connector to allow hot plug capability. Digital Diagnostic Functions are available via an I<sup>2</sup>C. It is with two independent 1310 nm laser transmitters. Each transmitter can transmit signal from 50Mbps to 3 Gbps with up to 30 km of single-mode fiber when using the SPS-9110VW-1RG/2RG as the receiver. A maximum distance of 10 km is achievable with 3Gbps pathological signals.

## I. Absolute Maximum Ratings

### Absolute Maximum Ratings

| Parameter                  | Symbol           | Min | Max | Unit   | Note           |
|----------------------------|------------------|-----|-----|--------|----------------|
| Storage Temperature        | T <sub>stg</sub> | -40 | 85  | °C     |                |
| Operating Case Temperature | T <sub>c</sub>   | -20 | 85  | °C     |                |
| Power Supply Voltage       | V <sub>cc</sub>  | 0   | 4   | V      |                |
| ESD Tolerance on all pins  |                  |     | 1   | KV HBM |                |
| Relative Humidity          |                  | 5   | 95  | % RH   | non-condensing |

## II. Recommended Operating Conditions

### Recommended Operating Conditions

| Parameter                  | Symbol          | Min  | Typ | Max  | Unit |
|----------------------------|-----------------|------|-----|------|------|
| Power Supply Voltage       | V <sub>cc</sub> | 3.13 | 3.3 | 3.47 | V    |
| Operating Case Temperature | T <sub>c</sub>  | 0    |     | 70   | °C   |
| Baud Rate                  |                 | 50   |     | 3000 | Mb/s |
| Power Supply Current       | I <sub>cc</sub> |      | 200 | 300  | mA   |

## III. Optical and Electrical Characteristics

### Transmitter Specifications (0°C < T<sub>c</sub> < 70°C, 3.13V < V<sub>cc</sub> < 3.47V)

| Parameter                     | Symbol            | Min  | Typ  | Max  | Unit  | Notes         |
|-------------------------------|-------------------|------|------|------|-------|---------------|
| <b>Optical</b>                |                   |      |      |      |       |               |
| Optical Transmit Power        | P <sub>o</sub>    | -5   |      | 0    | dBm   | 1, for TX1    |
|                               |                   | -8   |      | -3   | dBm   | 1, for TX2    |
| Output Center Wavelength      | λ <sub>TX1</sub>  | 1290 | 1310 | 1330 | nm    | At 25°C       |
|                               | λ <sub>TX2</sub>  | 1480 | 1550 | 1580 | nm    |               |
| Output Spectrum Width         | Δλ <sub>TX1</sub> |      | 1.5  | 3    | nm    | RMS (σ)       |
|                               | Δλ <sub>TX2</sub> |      |      | 1    | nm    | -20 dB width  |
| Extinction Ratio              | ER                | 5    | 7.5  |      | dB    |               |
| Relative Intensity Noise      | RIN               |      |      | -120 | dB/Hz |               |
| Optical Rise Time / Fall Time | tr / tf           |      |      | 135  | ps    | 2, SMPTE 424M |
|                               |                   |      |      | 270  | ps    | 2, SMPTE 424M |
|                               |                   |      |      | 800  | ps    | 2, SMPTE 424M |
|                               |                   |      |      | 1.5  | ns    | 2, SMPTE 424M |

| Parameter                     | Symbol            | Min | Typ | Max          | Unit  | Notes              |
|-------------------------------|-------------------|-----|-----|--------------|-------|--------------------|
| <b>Electrical</b>             |                   |     |     |              |       |                    |
| Differential Input Voltage    | $V_{IH} - V_{IL}$ | 200 | -   | 1200         | mVp-p | AC coupled input   |
| Disable Input Voltage -- Low  | $V_{TDIS,L}$      | 0   | -   | 0.8          | V     | TX Output Enabled  |
| Disable Input Voltage -- High | $V_{TDIS,H}$      | 2.0 | -   | $V_{CC}+0.3$ | V     | TX Output Disabled |
| SCL, SDA                      | $V_{OH}$          | 2.5 | -   | $V_{CC}+0.3$ | V     |                    |
|                               | $V_{OL}$          | 0   |     | 0.5          | V     |                    |

Notes

1. Output power is power coupled into a 9/125 mm single mode fiber.
2. 20% to 80%, unfiltered.

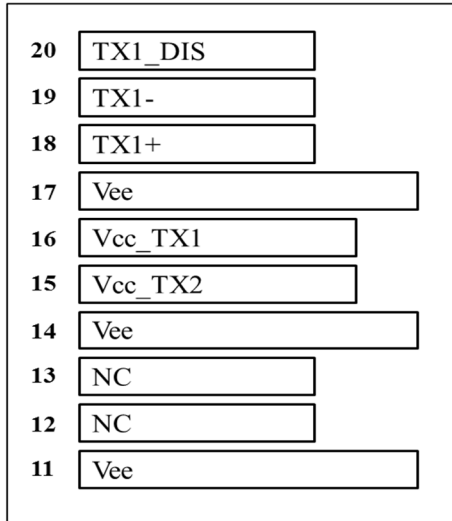
#### IV. Module Definition

| Module Definition | PIN 5 | PIN 6 | Interpretation by Host            |
|-------------------|-------|-------|-----------------------------------|
| 4                 | SCL   | SDA   | Serial module definition protocol |

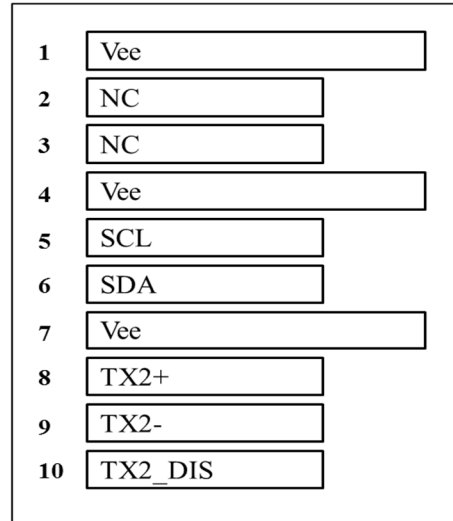
Module Definition 4 specifies a serial definition protocol. For this definition, upon power up, SCL and SDA appears as no connector (NC)

When the host system detects this condition, it activates the serial protocol. The protocol uses the 2-wire serial CMOS  $E^2$ PROM protocol of the ATMEL AT24C01A/02/04 family of components.

## V. Pin Information



Top of Board

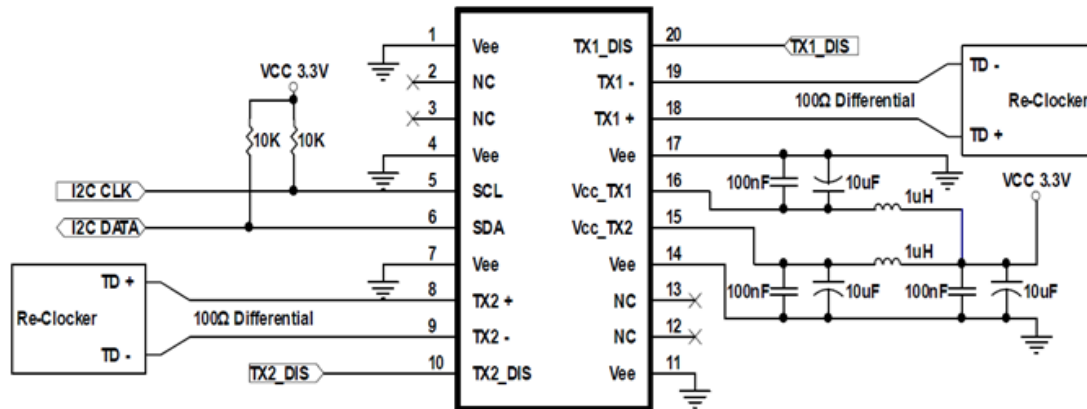
Bottom of Board  
(as viewed thru top of board)

| Pin | Name    | Function                         | NOTE                                                                                              |
|-----|---------|----------------------------------|---------------------------------------------------------------------------------------------------|
| 1   | Vee     | Signal Ground                    |                                                                                                   |
| 2   | NC      | No Connection                    |                                                                                                   |
| 3   | NC      | No Connection                    |                                                                                                   |
| 4   | Vee     | Signal Ground                    |                                                                                                   |
| 5   | SCL     | Serial I2C Clock                 |                                                                                                   |
| 6   | SDA     | Serial I2C Data                  |                                                                                                   |
| 7   | Vee     | Signal Ground                    |                                                                                                   |
| 8   | TX2+    | Positive Transmitter Data In (2) | AC coupled differential lines with 100 ohm differential termination inside the module             |
| 9   | TX2-    | Negative Transmitter Data In (2) |                                                                                                   |
| 10  | TX2_DIS | Transmitter Disable (2)          | Internal 4.7 kΩ pull-up. The bias current and modulation current are turned off if TX2_DIS =HIGH. |
| 11  | Vee     | Signal Ground                    |                                                                                                   |
| 12  | NC      | No Connection                    |                                                                                                   |
| 13  | NC      | No Connection                    |                                                                                                   |
| 14  | Vee     | Signal Ground                    |                                                                                                   |
| 15  | Vcc TX2 | Power Supply (2)                 | +3.3V±5%                                                                                          |
| 16  | Vcc TX1 | Power Supply (1)                 | +3.3V±5%                                                                                          |
| 17  | Vee     | Signal Ground                    |                                                                                                   |
| 18  | TX1+    | Positive Transmitter Data In (1) | AC coupled differential lines with 100 ohm differential termination inside the module             |
| 19  | TX1-    | Negative Transmitter Data In (1) |                                                                                                   |
| 20  | TX1_DIS | Transmitter Disable (1)          | Internal 4.7 kΩ pull-up. The bias current and modulation current are turned off if TX1_DIS        |

=HIGH

## VI. Application Schematics

### RECOMMENDED CIRCUIT SCHEMATIC



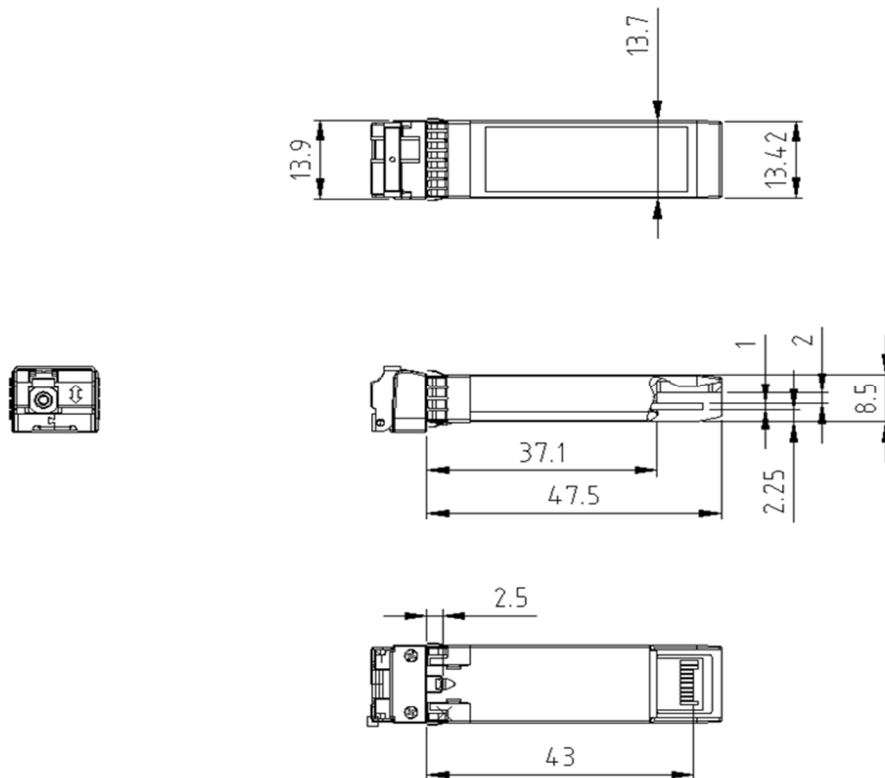
1. Consult the Chipset manufacturer's applications information for biasing required for TX outputs. Some chipset outputs are internally biased and may not need external bias resistor.

## VII. Outline Diagrams

### Package Outline

Dimensions are in millimeters

Tolerances are  $\pm 0.2\text{mm}$ , unless otherwise specified



### VIII. Ordering Information

| FVD       | Core      | - | Tx/Rx       | - | Mode             | Distance  |
|-----------|-----------|---|-------------|---|------------------|-----------|
| FOTTU     | 2 : 2core |   | 2T : 2Tx    | - | MM : Multimode   | 03 : 300m |
| Video SFP | 1: 1core  |   | 1T : 1Tx    |   | SM : Single mode | 10 : 10km |
| For SDI   |           |   | 2R : 2Rx    |   |                  | 30 : 30km |
|           |           |   | 1R : 1Rx    |   |                  | 50 : 50km |
|           |           |   | 1TR:1Tx+1Rx |   |                  | 80 : 80km |
|           |           |   | 1B : 1BOSA  |   |                  |           |
|           |           |   | 2B : 2BOSA  |   |                  |           |

### IX. More information

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